Application No.	Applicant(s)	
10/786.333	FUKUSHLET AL.	
Examiner	Art Unit	<u> </u>
Vu A Le	2824	
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	v (PTO-948) attached	
ne header according to 37 CF	R 1.121(d).	
6. ☐ Interview S Paper No. 8), 7. ☐ Examiner's 8. ☑ Examiner's	ummary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allowance	
	Examiner Vu A. Le Pars on the cover sheet wire (OR REMAINS) CLOSED in or other appropriate comming GHTS. This application is said MPEP 1308. The Examiner. The der 35 U.S.C. § 119(a)-(d) or other appropriate comming GHTS. This application is said MPEP 1308. The Examiner. The der 35 U.S.C. § 119(a)-(d) or other appropriate communication to file in the communication to file in the same of this application. The der according to the submitted on the header according to 37 CF is of BIOLOGICAL MATER O	Total this communication to file a reply complying with the requirements extended. Note the attached EXAMINER'S AMENDMENT or NOTICE Of its reason(s) why the oath or declaration is deficient. It is submitted. On's Patent Drawing Review (PTO-948) attached Art Unit 2824 Pars on the cover sheet with the correspondence address— (OR REMAINS) CLOSED in this application. If not included or other appropriate communication will be mailed in due course. To get 35 U.S.C. This application is subject to withdrawal from issue at the in and MPEP 1308. The Examiner. Inder 35 U.S.C. § 119(a)-(d) or (f). The examiner. Inder 35 U.S.C. § 119(a)-(d) or (f). The examiner. Inder 35 U.S.C. § 119(a)-(d) or (f). The examiner. Inder 35 U.S.C. § 119(a)-(d) or (f). The examiner. Inder 35 U.S.C. § 119(a)-(d) or (f). The examiner. Inder 35 U.S.C. § 119(a)-(d) or (f). The examiner. Inder 35 U.S.C. § 119(a)-(d) or (f). The examiner. Inder 35 U.S.C. § 119(a)-(d) or (f). The examiner. Inder 35 U.S.C. § 119(a)-(d) or (f). The examiner. Inder 35 U.S.C. § 119(a)-(d) or (f). Inder 190 and 19

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REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance: the present invention relates to ferroelectric memory device. The independent clams 1 and 7 recite a memory device having a potential detecting circuit for detecting a potential on a bit line exceeds a predetermined potential and outputting a detection signal and a sense amplifier which starts amplifying the potential on said bit line in response to the detection signal. The PRIO ART fails disclose or suggest such a potential detecting circuit for a memory device as described in the independent claims 1 and 7, therefore, claims 1-10 are in condition for allowance.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vu A. Le whose telephone number is (571) 272-1871. The examiner can normally be reached on M-F (7:00-3:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Vu A. Le

Primary Examiner

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09/28/04